

FIG. 1

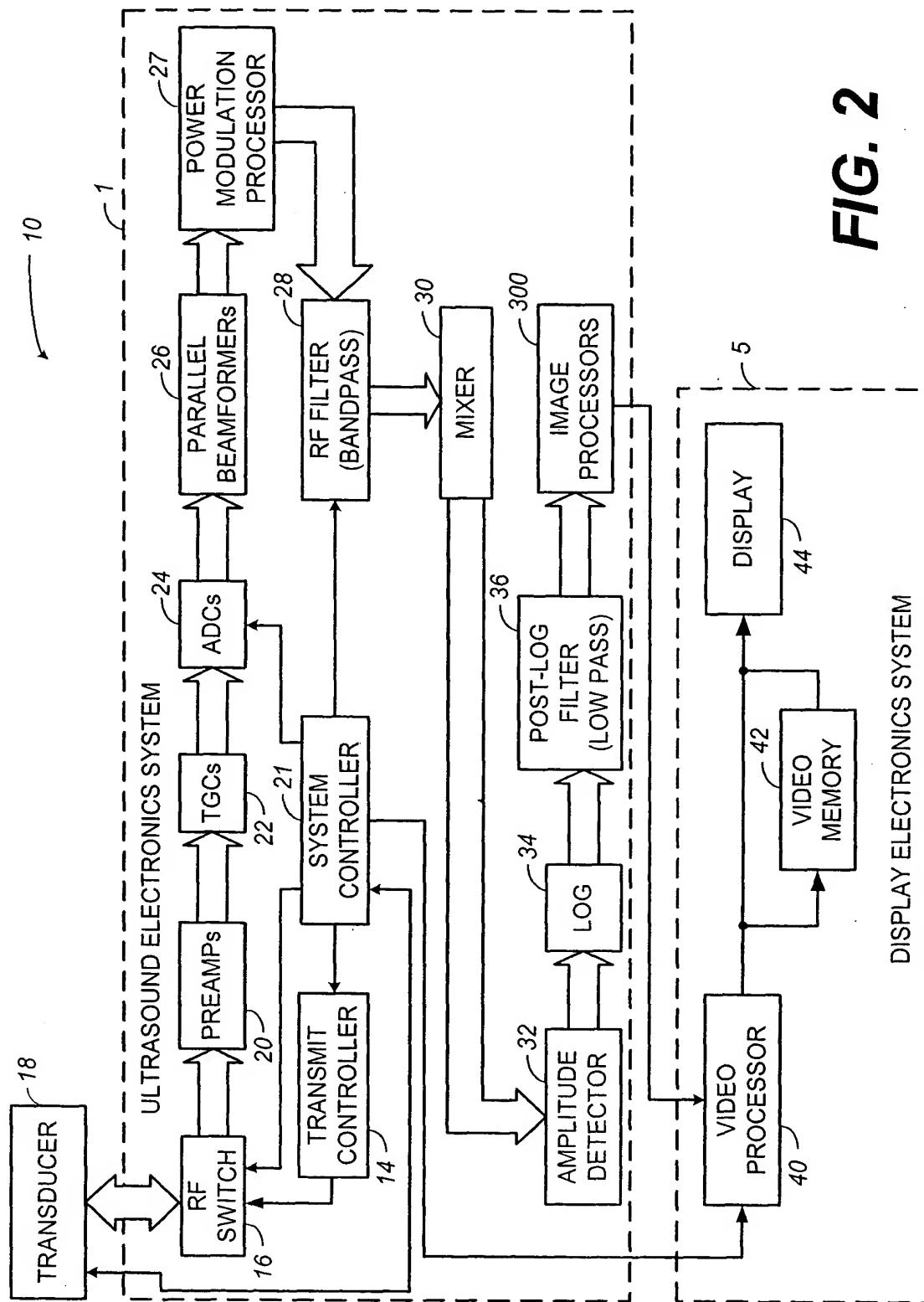


FIG. 2

DISPLAY ELECTRONICS SYSTEM

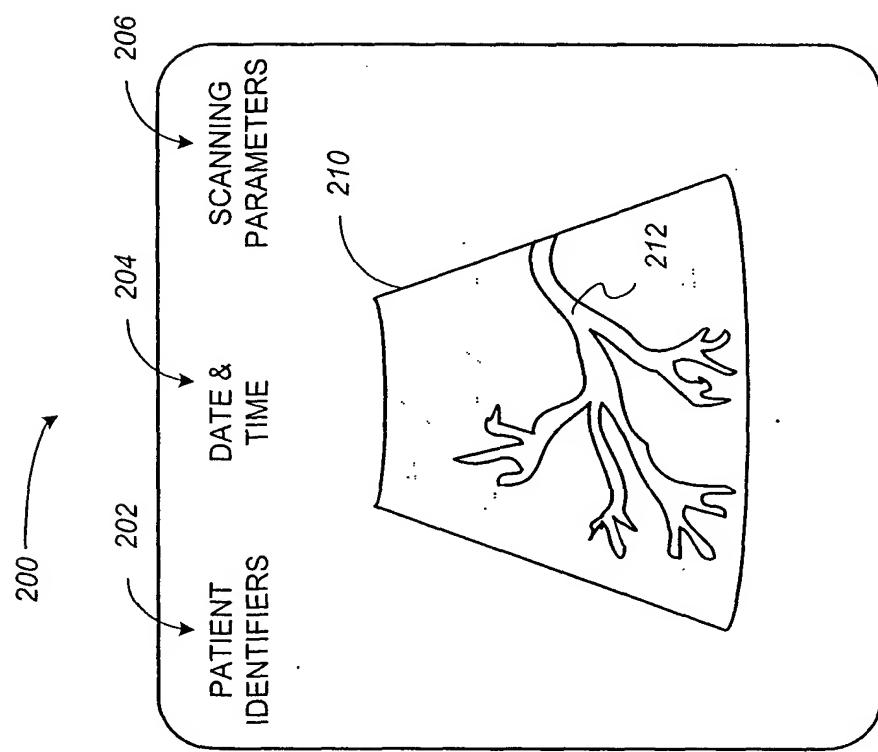
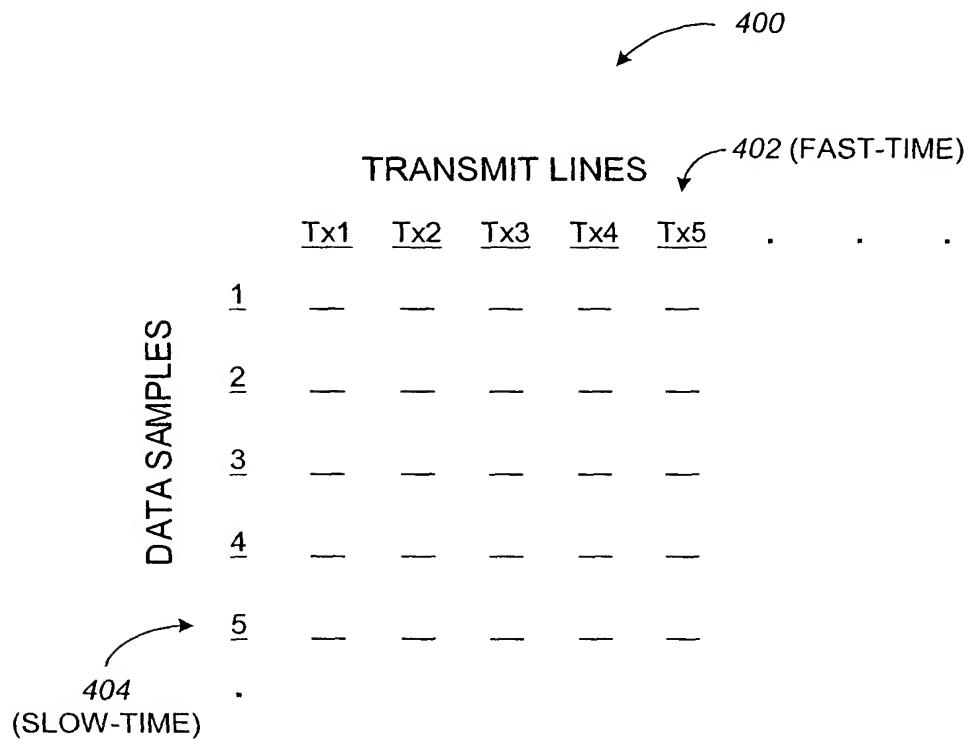


FIG. 3



POWER MODULATED MULTI-LINE SUBPACKET

0 L H L 0
0 -1 1 -1 0 (FIR FILTER WEIGHTS)

WHERE, 0 INDICATES NO TRANSMIT PULSE

L INDICATES A HALF-POWER TRANSMIT PULSE

H INDICATES A FULL-POWER TRANSMIT PULSE

FIG. 4

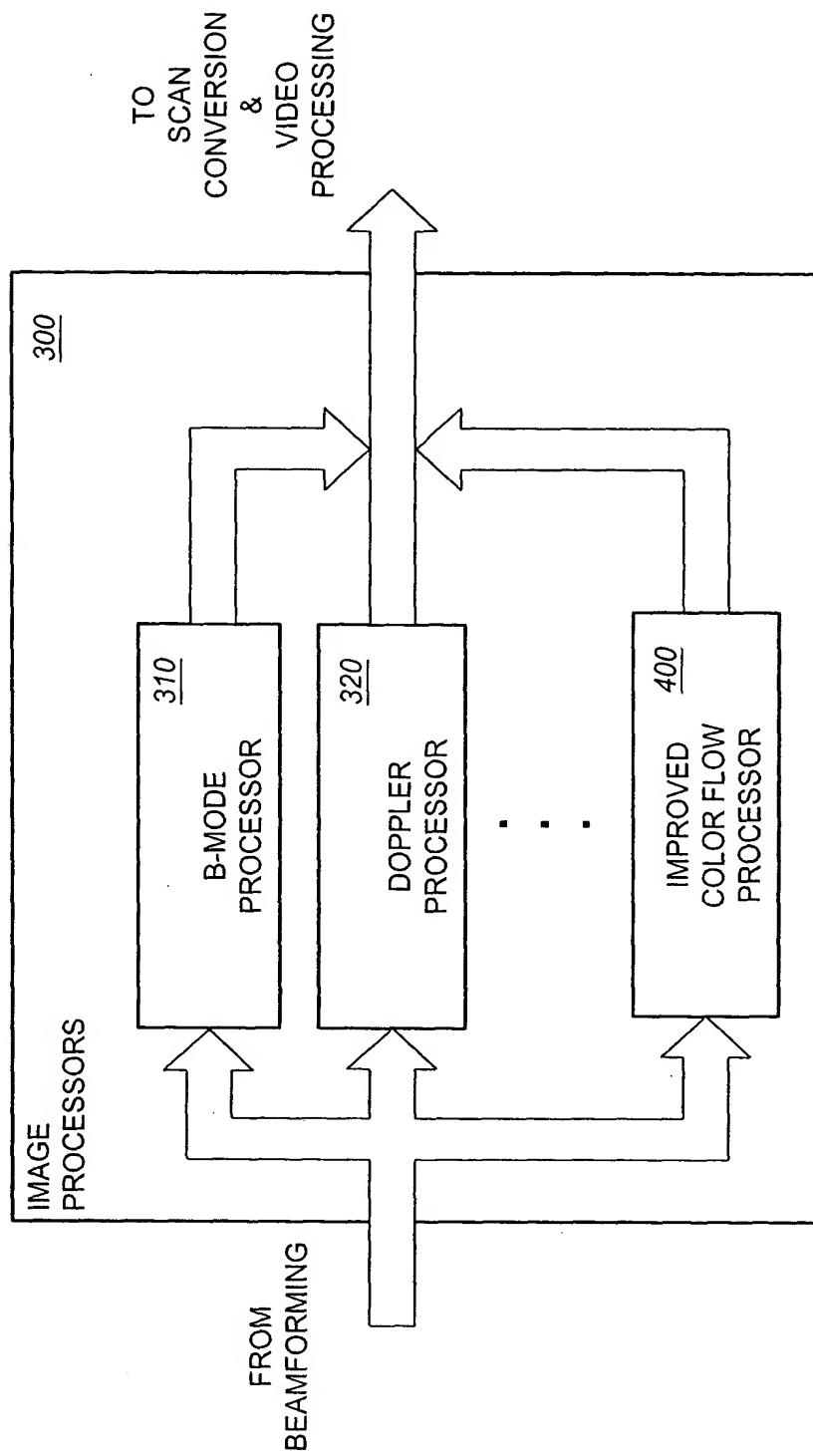


FIG. 5

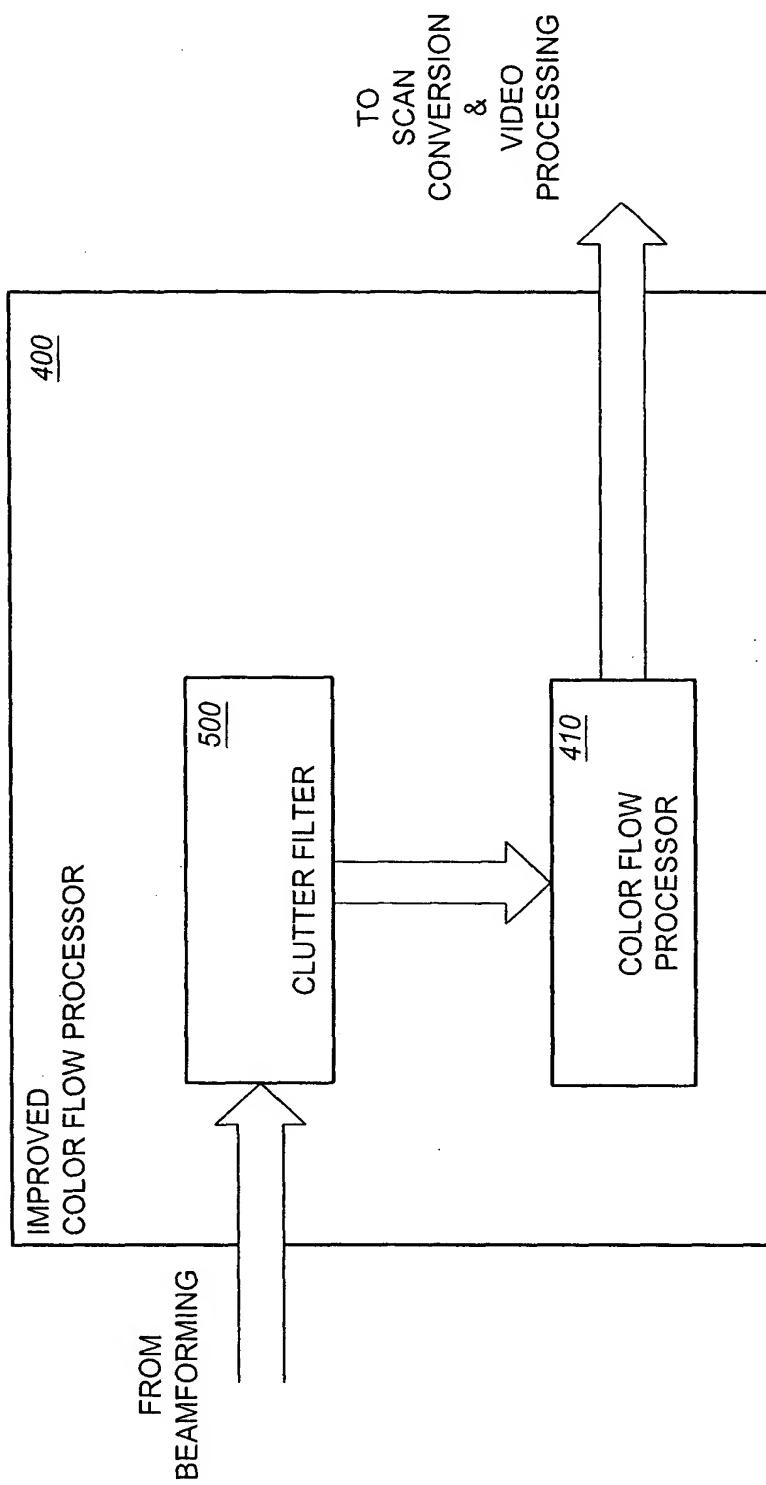


FIG. 6

POWER MODULATED TRANSMIT SEQUENCE

0 L H L 0 L H L 0 L H L 0

WHERE, 0 INDICATES NO TRANSMIT PULSE
L INDICATES A HALF-POWER TRANSMIT PULSE
H INDICATES A FULL-POWER TRANSMIT PULSE

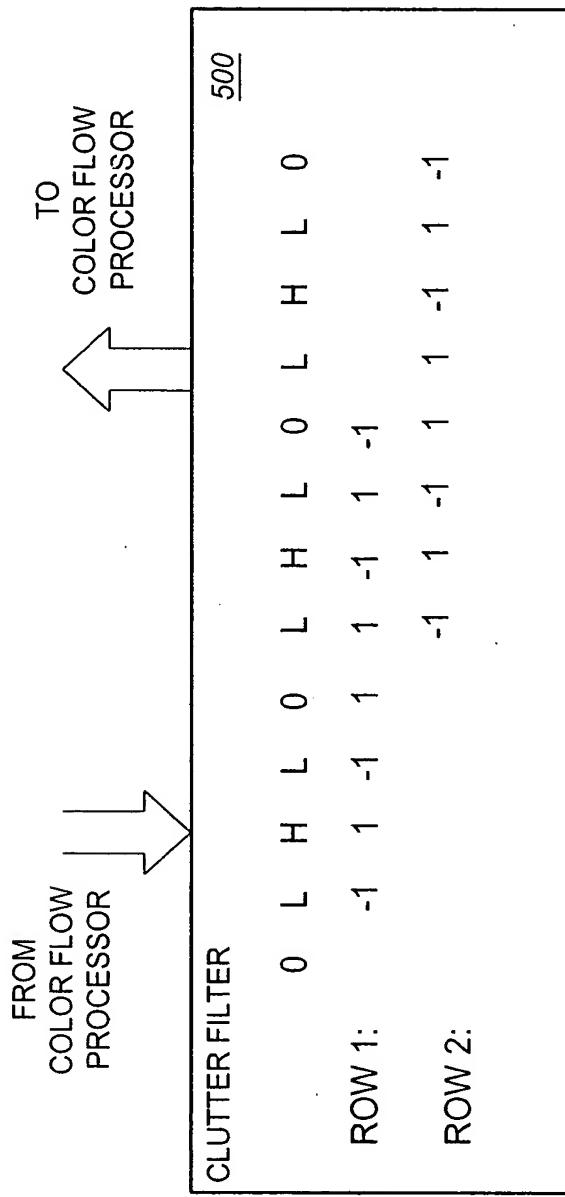


FIG. 7

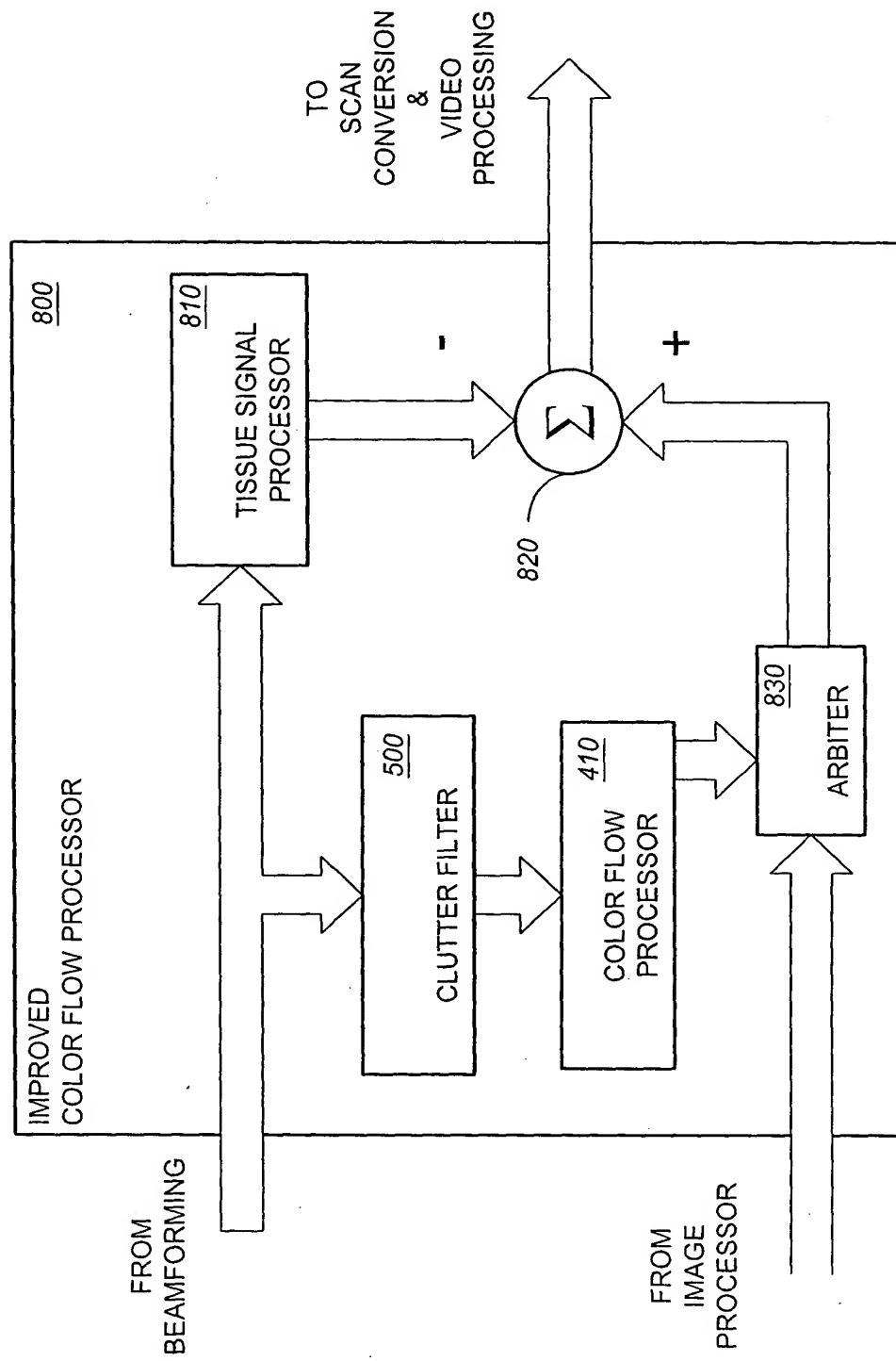


FIG. 8

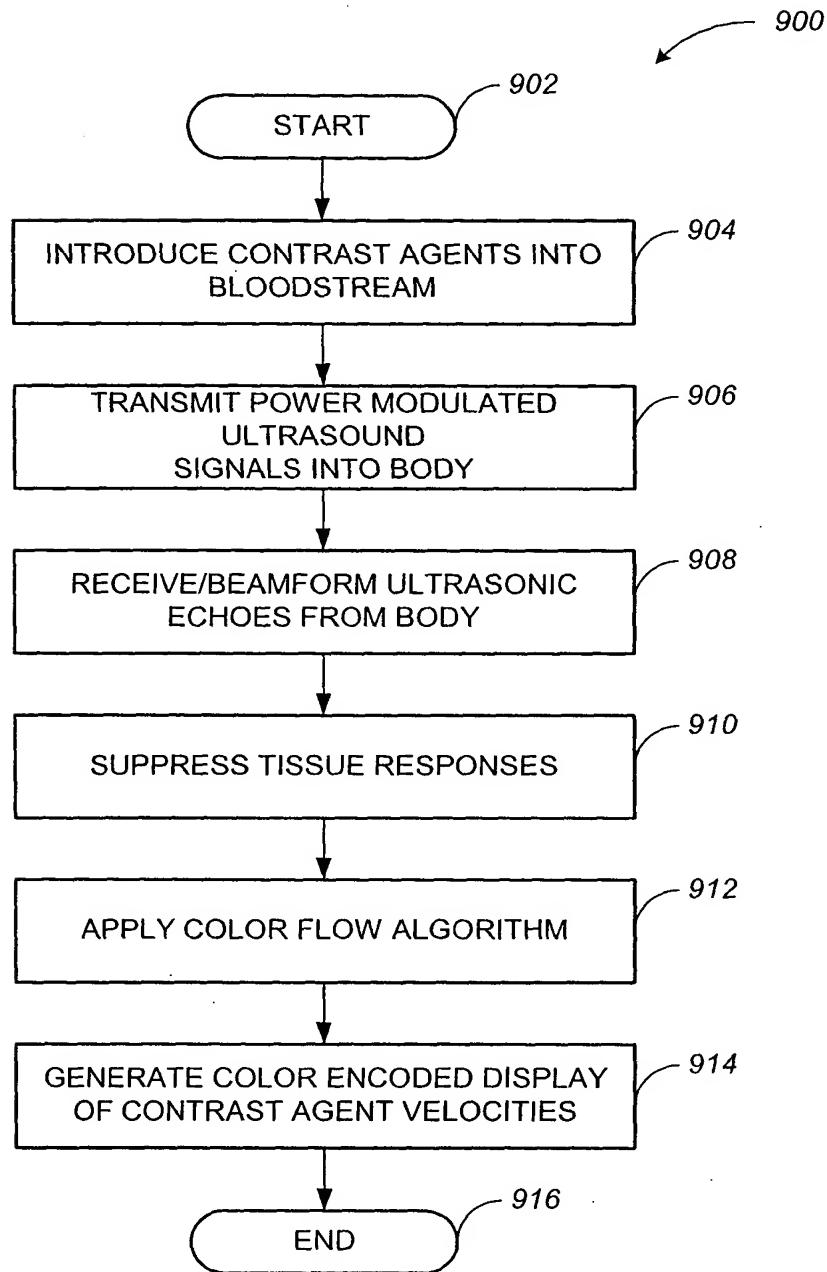


FIG. 9

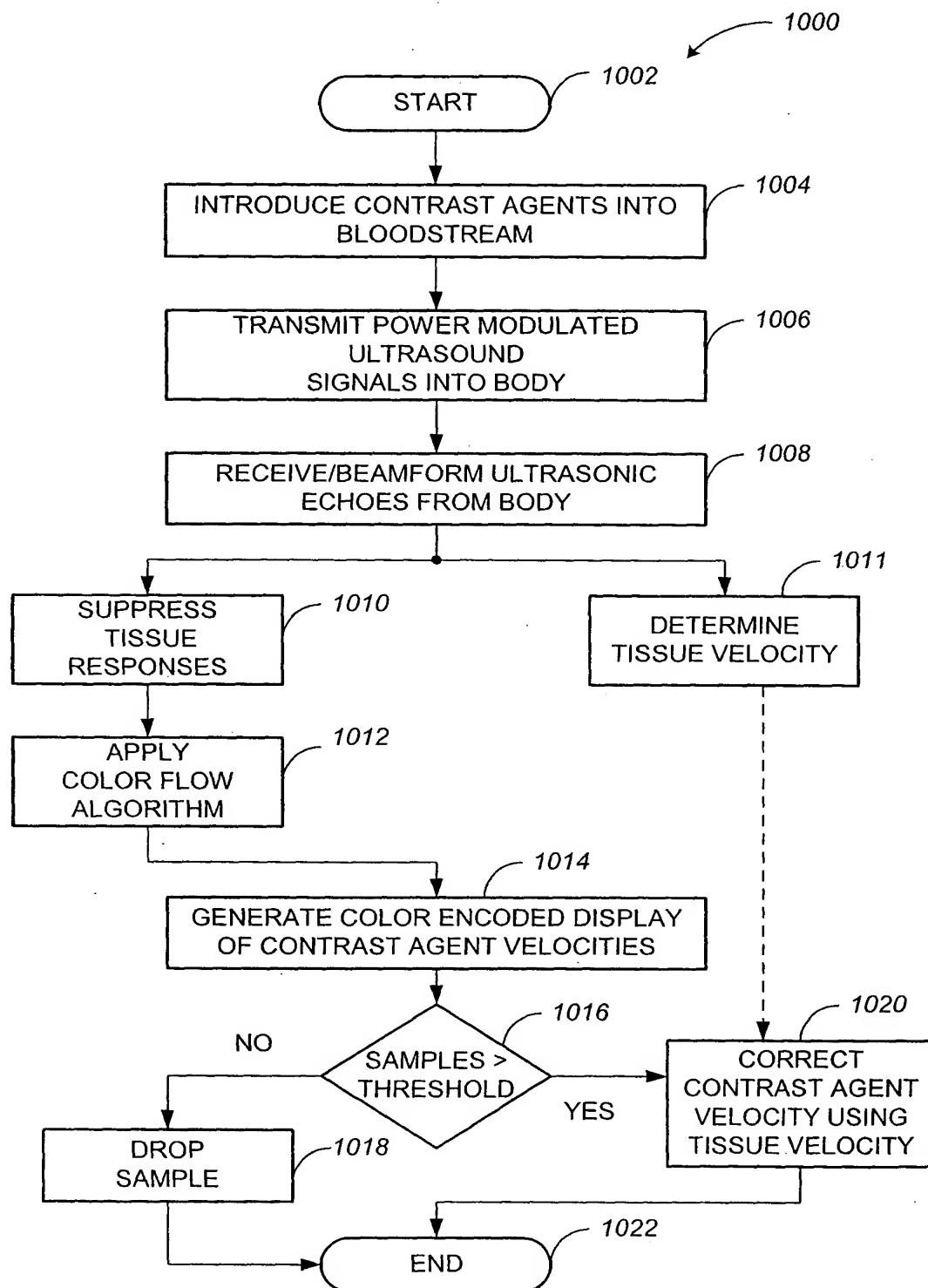


FIG. 10